

- ☞ L46: (46) 39 and 44
- ☞ L46: (454) 8 same 9
- ☞ L47: (6) 30 and 46
- ☞ L48: (3) 29 and 46
- ☞ L49: (33) 28 and 46
- ☞ L50: (129) 46 and 22
- ☞ L51: (103) 46 and 23
- ☞ L52: (84) 46 and 24
- ☞ L53: (10) 46 and 26
- ☞ L54: (22) 46 and 26
- ☞ L55: (7) 46 and 27
- ☞ L57: (124750) monitor\$6 near5 (temperature or voltage)
- ☞ L58: (87) 13 and 57
- ☞ L61: (639) 12 and 57
- ☞ L62: (71) 12 same 57
- ☞ L63: (2) 57 and 46
- ☞ L64: (16) 8 and 61
- ☞ L67: (1337) 9 same 57
- ☞ L70: (7190) 9 and 57
- ☞ L71: (112) 8 and 70
- ☞ L73: (24) monitor\$6 with 13
- ☞ L74: (49) monitor\$6 same 13
- ☞ L75: (25) 74 not 73
- ☞ L77: (761) (SMITH near2 KENNETH).In.
- ☞ L78: (239) (PERNER near2 FREDERICK).In.
- ☞ L79: (102) (SESEK near2 ROBERT).In.
- ☞ L76: (4) (LEMUS near2 CONNIE).In.

Search:

DBs: US-PG-PUB; US-PAT; EPO; JPO; DERWENT; IBM; TDB

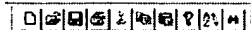
☐ BasicDefault generator: OR☐ Highlight all hit terms initiallyBRS form

	U	Document ID	Inventor	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	S	C
1	<input type="checkbox"/>	US 6586989 B2	Perner; Frederick A. et al.	20030701	15	Nonlinear digital differential amplifier offset calibration	330/9	330/2; 330/253		<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	US 6262626 B1	Perner; Frederick A. et al.	20010717	14	Operational amplifier with digital offset calibration	330/2	330/253		<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	US 6700814 B1	Nahas; Joseph J. et al.	20040302	13	Sense amplifier bias circuit for a memory having at least two digital offset calibration	365/168	365/207; 365/210		<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	US 6233190 B1	Cooper; Christopher B. et al.	20010515	15	Method of storing a temperature threshold in an integrated circuit	365/212	365/211		<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	US 6791874 B2	Tran; Lung T. et al.	20040914	15	Memory device capable of calibration and calibration method	365/168	365/211; 365/242		<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	US 6868026 B2	Hsu; Sheng Teng	20050315	33	Temperature compensated SRAM circuit	365/211	365/213		<input type="checkbox"/>	<input type="checkbox"/>

Hits

Ready

NUM



Active

- L8: (33659) magnetic adj3 memory
- L9: (131961) (sens\$4 or differential) adj2 amplifier
- L12: (7036) adjust\$6 with 9
- L13: (724) calibrat\$6 with 9
- L14: (9) 8 same 12
- L16: (12) 8 same 13
- L22: (1506) 366/168
- L23: (1163) 366/174
- L24: (1047) 366/173
- L26: (3019) 366/206
- L26: (3124) 366/207
- L27: (1986) 366/208
- L28: (202) 366/209
- L29: (209) 366/211
- L30: (46) 366/213
- L31: (18) 28 and 12
- L32: (10) 28 and 13
- L36: (37) 8 and 13
- L38: (103) 8 and 12
- L39: (366) calibrat\$6 near5 9
- L43: (12) (supply adj voltage) same 39
- L42: (43) temperature same 39
- L44: (59627) "366"/\$6
- L45: (46) 39 and 44
- L46: (464) 8 same 9
- L47: (6) 30 and 46
- L48: (3) 29 and 46
- L49: (33) 28 and 46
- L50: (129) 46 and 22
- L51: (103) 46 and 23
- L52: (84) 46 and 24
- L53: (10) 46 and 25
- L54: (22) 46 and 26
- L55: (7) 46 and 27
- L57: (124750) monitor\$6 near5 (temperature or voltage)
- L59: (87) 13 and 57
- L61: (639) 12 and 57
- L62: (71) 12 same 57

Status: LV [Source] [Run] [Exit]

DB: US-PGPUB;USPAT;EPO;JPO;DERWENT;IBM_TDB

☒ Purge

Default generator: OR

☒ Highlight all hit terms initially

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Ready

NUM